

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventorship Zerbe
 Applicant Rambus Inc.
 Attorney's Docket No. RB1-040US
 Title: Signal Line Routing to Reduce Crosstalk Effects

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled "Semiconductor Stacked Die Devices and Methods of Forming Semiconductor Stacked Die Devices," the specification of which is attached hereto.

I have reviewed and understand the content of the above-identified specification, including the claims.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

PRIOR FOREIGN APPLICATIONS: no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

Power of Attorney

I appoint the following attorneys to prosecute this application and transact all future business in the Patent and Trademark Office connected with this application:
 Lewis C. Lee, Reg. No. 34,656; Daniel L. Hayes, Reg. No. 34,618; Allan T.

1 Sponseller, Reg. 38,318; Steven R. Sponseller, Reg. No. 39,384; James R.
2 Banowsky, Reg. No. 37,773; Lance R. Sadler, Reg. No. 38,605; Thomas A. Jolly,
3 Reg. No. 39,241; Kasey C. Christie, Reg. No. 40,559, Nathan R. Rieth, Reg. No.
4 44,302; Brian G. Hart, Reg. No. 44,421 David S. Thompson, Reg. No. 37,954; Paul
5 W. Mitchell, Reg. No. 44,453; Bradley K. DeSandro, Reg. No. 34,521 Brian J.
6 Pangrle, Reg. No. 42,973, David A. Morasch, Reg. No. 42,905, Robert R. Axenfeld,
7 Reg. No. 37,276, and Keith W. Saunders, Reg. No. 41,462.

8 Send correspondence to: **Customer Number: 29150.**

9 Direct telephone calls to: Daniel L. Hayes (509) 324-9256.

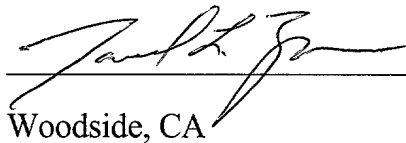


10
11 All statements made herein of my own knowledge are true and that all
12 statements made on information and belief are believed to be true; and further that
13 these statements were made with the knowledge that willful false statements and the
14 like so made are punishable by fine or imprisonment, or both, under Section 1001 of
15 Title 18 of the United States Code and that such willful false statement may
16 jeopardize the validity of the application or any patent issued therefrom.

17
18 * * * * *

19 Full name of inventor: Jared L. Zerbe

20 Inventor's Signature



Date: 11/13/01

21 Residence:

Woodside, CA

22 Citizenship:

USA

23 Post Office Address:

431 Hillside Dr.
Woodside, CA 94062